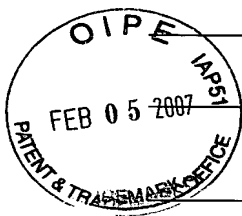


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1-30-07
(Date of Deposit)

Joseph F. Hetz, Reg. No. 41,070

Name of applicant, assignee, or
Registered Representative

Signature

1-30-07
Date of Signature

Our File No. 10519-57

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of:

Scott Brad Herner et al.

Patent No.: 7,148,570 B2

Issue Date: December 12, 2006

Serial No.: 09/928,975

Filing Date: August 13, 2001

For: LOW RESISTIVITY TITANIUM SILICIDE
ON HEAVILY DOPED SEMICONDUCTOR

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) Examiner: Nitin Parekh
)
) Group Art Unit: 2811
)
)
)
)
)

REQUEST FOR CERTIFICATE OF CORRECTION

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Attn: Certificate of Corrections Branch

Sir:

Please issue a Certificate of Correction for the above-identified patent to correct the errors listed on the accompanying PTO Form 1050. This request is being made pursuant to 35 U.S.C. § 254, since the errors comprise minor or typographical errors by the Patent and Trademark Office.

Certificate

FEB 08 2007

of Correction

FEB 09 2007

The Commissioner is hereby authorized to charge any fees required to Deposit Account No. 23-1925. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

A handwritten signature in black ink, appearing to read 'Joseph F. Hetz', is written over a horizontal line.

Joseph F. Hetz
Registration No. 41,070
Attorney for Applicants

BRINKS HOFER GILSON & LIONE
P.O. Box 10395
Chicago, Illinois 60610
(312) 321-4200

FEB 09 2007

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PATENT NO : 7,148,570 B2
APPLICATION NO : 09/928,975
ISSUE DATE : December 12, 2006
INVENTOR(S) : Scott Brad Herner et al.

Page 1 of 2

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims

Column 8, in claim 2, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 3, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 4, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 5, line 1, before "of claim 1" delete "invention" and substitute --semiconductor structure-- in its place.

Column 8, in claim 6, line 1, before "of claim 1 or 5" delete "invention" and substitute --semiconductor structure-- in its place.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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Chicago, Illinois 60610

PATENT NO. 7,148,570 B2

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PATENT NO : 7,148,570 B2
APPLICATION NO. : 09/928,975
ISSUE DATE : December 12, 2006
INVENTOR(S) : Scott Brad Herner et al.

Page 2 of 2

It is certified that error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Claims (cont'd)

Column 8, in claim 7, line 9, delete "squarel" and substitute --square-- in its place.

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